



In re Application of:

KARSTEN WIECZOREK ROLF GEILENKEUSER JÖRG-OLIVER WEIDNER

Serial No.: 10/649,051

Filed: August 27, 2003

Mail Stop Amendment

P.O. Box 1450

Commissioner for Patents

Alexandria, VA 22313-1450

For: INTEGRATED SEMICONDUCTOR

STRUCTURE FOR RELIABILITY

TESTS OF DIELECTRICS

Examiner: Jesse A. Fenty

Group Art Unit: 2815

Att'y Docket: 2000.108400/DE0349

Customer No.: 23720

RESPONSE TO RESTRICTION REQUIREMENT DATED OCTOBER 5, 2004

CERTIFICATE OF MAILING

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as "FIRST CLASS MAIL" addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, this 26th day of October, 2004.

Signature

Sir:

This paper is submitted in response to the Restriction Requirement dated October 5, 2004, for which the one-month date for response is November 5, 2004.

In response to the restriction requirement which the Examiner imposed, Applicants elect, with traverse, to prosecute claims 12-23, *i.e.*, the Group II claims.

It is believed that no fee is due; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason relating to this document, the Director is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/DE0349. In the event the monies in that account are insufficient, the Director is authorized to withdraw_funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.108400.

The Examiner is invited to contact the undersigned attorney at (713) 934-4055 with any questions, comments or suggestions relating to the referenced patent application.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON

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Date: October 26, 2004

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